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## \*BIBDATASHEET\*

Bib Data Sheet

**CONFIRMATION NO. 62** 

SERIAL NUMBER 09/998,595	FILING DATE 11/16/2001 C RULE 1.47		ASS 57	GROUP ART UNIT 2822		ATTORNEY DOCKE NO. 93-C-032RE(1678-4		
APPLICANTS								
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** CONTINUING DATA **********************************								
Foreign Priority claimed			STATE OR	SHEETS		ТО	TAL .	INDEPENDE
35 USC 119 (a-d) conditions met ves Met after Allowance  Verified and Acknowledged Examiner's Signature Initials			COUNTRY OR	DRA			AIMS 48	CLAIMS 6
ADDRESS LISA K. JORGENSON STMICROELECTRONICS, INC. 1310 ELECTRONICS DRIVE CARROLLTON, TX 75006-5039								
TITLE Enhanced planarization technique for an integrated circuit								
					☐ All Fees ☐ 1.16 Fees (Filing)			
FILING FEE FEES:	Authority has been given	CCOUNT		1.17 Fees ( Processing Ext. of time				
RECEIVED No. —	to charge/credit	DEFUSII AC	ACCOUNT		1.18 Fees (Issue)			
1506				Other				
					☐ Credit			